Roll No.

Total No. of Pages : 02

Total No. of Questions : 07

BCA (Sem-2) COMPUTER SYSTEM ARCHITECTURE Subject Code : BSBC-204 (2011 Batch) Paper ID : [B1116]

Time : 3 Hrs.

Max. Marks: 60

INSTRUCTION TO CANDIDATES :

- 1. SECTION-A is COMPULSORY.
- 2. Attempt any FOUR questions from SECTION-B.

SECTION-A $(10 \times 2 = 20 \text{ Marks})$

- 1. (a) How SIMD is different from MIMD?
 - (b) What is register transfer language?
 - (c) List various addressing modes.
 - (d) What are serial and parallel ports?
 - (e) What is significance of hit ratio in cache memory?
 - (f) What is three address instructions? Illustrate with example.
 - (g) What is memory stack?
 - (h) What is strobe and handshaking?
 - (i) What is logical and physical address space?
 - (j) Explain how isolated I/O is different from memory mapped I/O?

SECTION-B $(4 \times 10 = 40 \text{ Marks})$

- 2. What is bus? What are its types? Explain the working of 16 bit common bus system.
- 3. What is interrupt? What are its types? Explain the working of interrupt cycle.

- 4. What is meant by control unit? What are various functions? Draw and explain the working of micro programmed control unit.
- 5. Explain the following :
 - (a) DMA Controller
 - (b) I/O interface unit
- 6. What is cache memory ? What are its advantages? Explain how data is written into cache?
- 7. Write notes on the following :
 - (a) Locality of reference
 - (b) mobile device architectures

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